

65V N-Channel MOSFETs

PPAK5X6 Pin Configuration

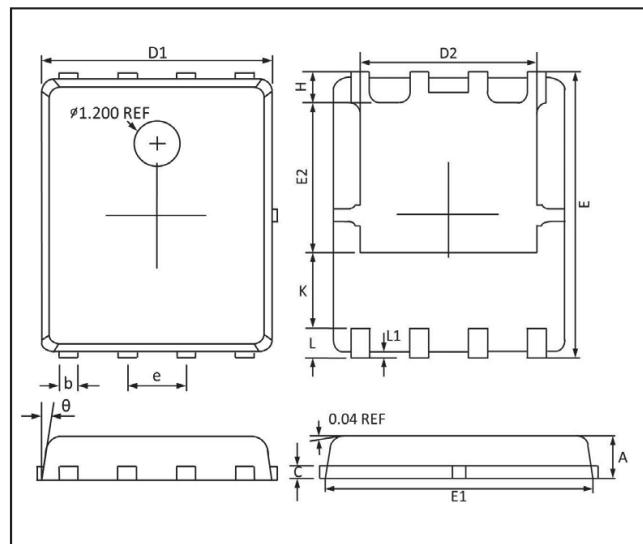
BVDSS	RDS(ON)	ID
65V	4.4mΩ	95A

Features

- 65V, 95A, $R_{DS(ON)} = 4.4\text{m}\Omega$ @ $V_{GS} = 10\text{V}$
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Networking
- Load Switch
- LED applications



MAXIMUM RATINGS AND CHARACTERISTICS

@ 25°C Ambient Temperature (unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	65	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current – Continuous ($T_c=25^\circ\text{C}$)	I_D	95	A
Drain Current – Continuous ($T_c=100^\circ\text{C}$)	I_D	60	A
Drain Current – Pulsed ¹	I_{DM}	380	A
Single Pulse Avalanche Energy ²	EAS	151.3	mJ
Single Pulse Avalanche Current ²	IAS	55	A
Power Dissipation ($T_c=25^\circ\text{C}$)	P_D	96	W
Power Dissipation – Derate above 25°C		0.77	W/ $^\circ\text{C}$
Storage Temperature Range	T_{STG}	-50 to 150	$^\circ\text{C}$
Operating Junction Temperature Range	T_J	-50 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance Junction to ambient	$R_{\theta JA}$	---	62	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction to Case	$R_{\theta JC}$	---	1.3	$^\circ\text{C}/\text{W}$

MOSFET ELECTRICAL CHARACTERISTICS $T_A=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	65	---	---	V
Drain-Source Leakage Current	I_{DS}	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=48\text{V}, V_{\text{GS}}=0\text{V}, T_J=85^\circ\text{C}$	---	---	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$	---	3.7	4.4	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=15\text{A}$	---	5.8	7.5	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	1	1.6	2.5	V
Forward Transconductance	g_{fs}	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=3\text{A}$	---	10	---	S

Dynamic and switching Characteristics

Total Gate Charge ^{3, 4}	Q_g	$V_{\text{DS}}=48\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=30\text{A}$	---	36	54	nC
Gate-Source Charge ^{3, 4}	Q_{gs}		---	4.7	7.1	
Gate-Drain Charge ^{3, 4}	Q_{gd}		---	13.5	20	
Turn-On Delay Time ^{3, 4}	$T_{\text{d}(\text{on})}$	$V_{\text{DD}}=48\text{V}, V_{\text{GS}}=10\text{V}, R_G=6\Omega, I_{\text{D}}=1\text{A}$	---	10.2	15	ns
Rise Time ^{3, 4}	T_r		---	16	24	
Turn-Off Delay Time ^{3, 4}	$T_{\text{d}(\text{off})}$		---	42	63	
Fall Time ^{3, 4}	T_f		---	38	57	
Input Capacitance	C_{iss}		---	1675	2510	pF
Output Capacitance	C_{oss}	$V_{\text{DS}}=48\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	---	322	485	
Reverse Transfer Capacitance	C_{rss}		---	14	25	
Gate resistance	R_g	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	---	1.2	---	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_s	$V_G=V_D=0\text{V}, \text{Force Current}$	---	---	95	A
Pulsed Source Current	I_{SM}		---	---	190	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_s=1\text{A}, T_J=25^\circ\text{C}$	---	---	1	V
Reverse Recovery Time ³	t_{rr}	$V_R=50\text{V}, I_s=10\text{A}$ $dI/dt=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$	---	54	---	ns
Reverse Recovery Charge ³	Q_{rr}		---	67	---	nC

Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=55\text{A}, R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.
- The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.

RATINGS AND CHARACTERISTIC CURVES

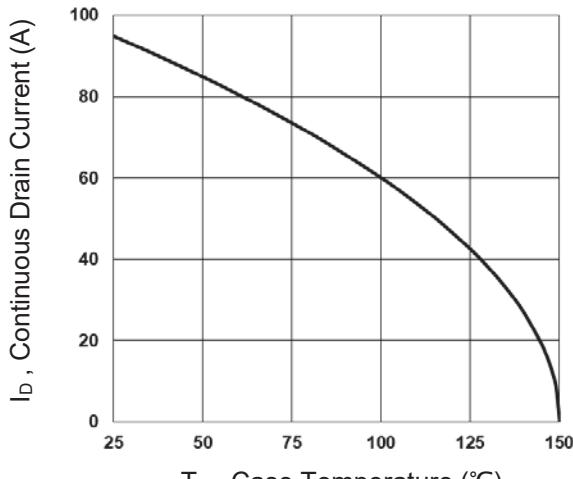


Fig.1 Continuous Drain Current vs. T_c

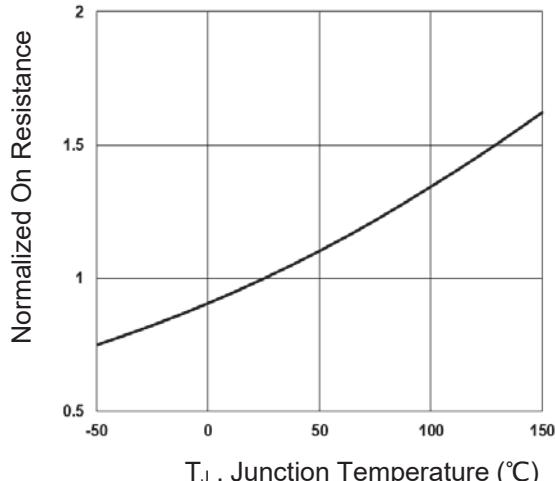


Fig.2 Normalized RDS(ON) vs. T_j

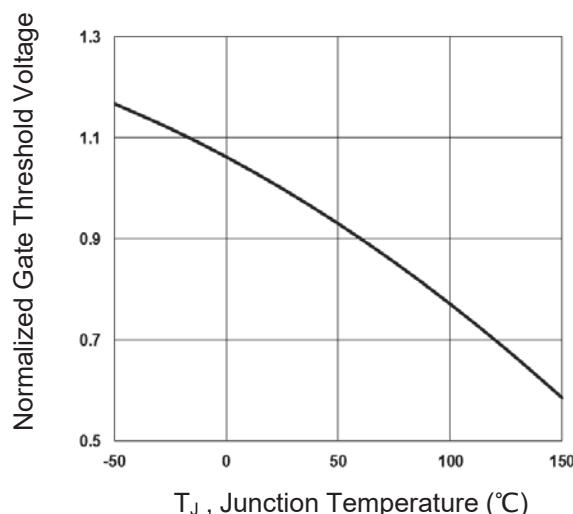


Fig.3 Normalized V_{th} vs. T_j

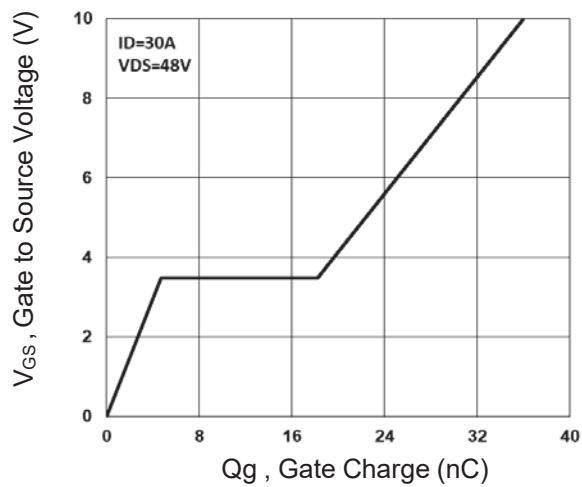


Fig.4 Gate Charge Characteristics

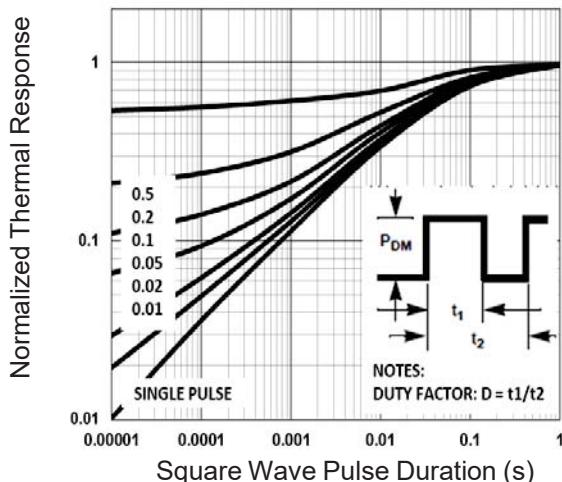


Fig.5 Normalized Transient Impedance

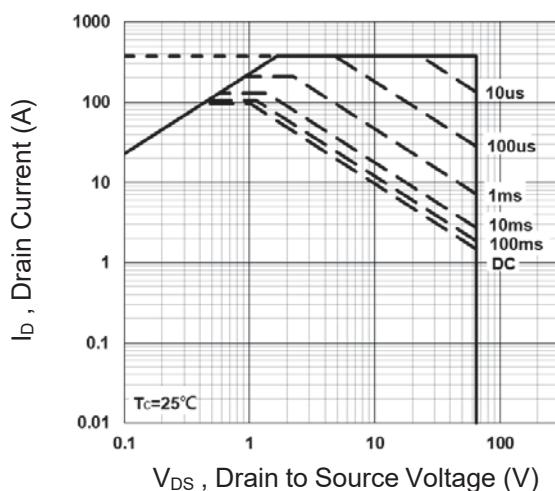


Fig.6 Maximum Safe Operation Area

RATINGS AND CHARACTERISTIC CURVES

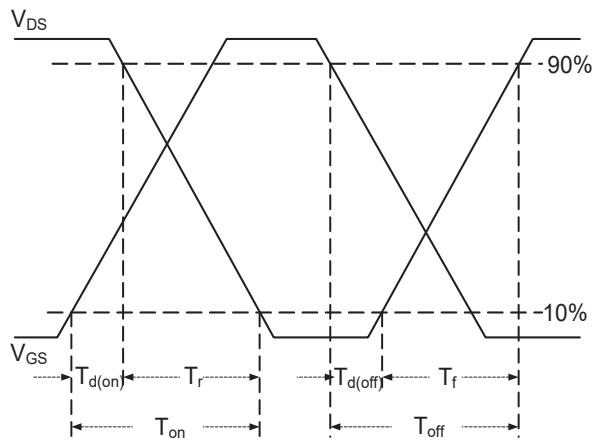


Fig.7 Switching Time Waveform

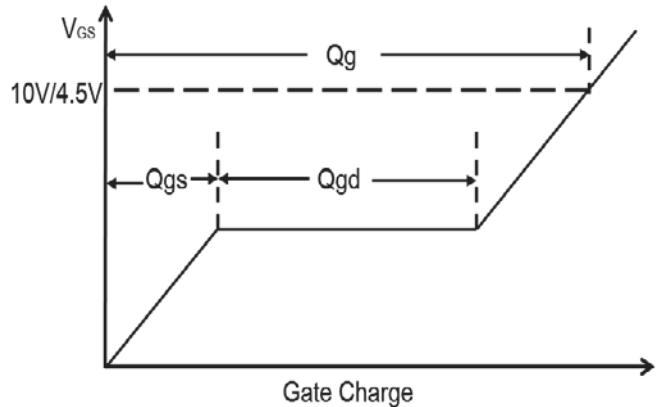


Fig.8 Gate Charge Waveform